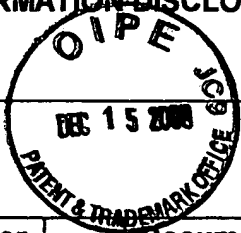


INFORMATION DISCLOSURE STATEMENT 	Case No.	21-9-8-5
	Serial No.	09/660,025
	Applicant:	F. M. Chiussi et al
	Filing Date:	September 12, 2000
	Group:	2664


U.S. PATENT DOCUMENTS

*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
	AA					
	AB					
	AC					
	AD					
	AE					

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation
	AF					
	AG					
	AH					


OTHER (including Author, Title, Date, Pertinent Pages, etc.)

	AI	F. M. Chiussi et al, "Low-Cost Scalable Switching Solution for Broadband Networking: The ATLANTA Architecture and Chipset", <i>IEEE Communications Magazine</i> , December 1997, pp 44-53
	AJ	F. M. Chiussi et al, "Backpressure In Shared-Memory-Based ATM Switches Under Multiplexed Bursty Sources", <i>Proc. IEEE INFOCOM '96 - Networking The Next Generation</i> , Vol. 2, pp 830-843, San Francisco, CA, March 1996
	AK	D. C. Stephens et al, "Implementing Distributed Packet Fair Queueing In A Scalable Switch Architecture", <i>Proc. IEEE INFOCOM '98 - Gateway to the 21st Century</i> , Vol. 1, pp 282-290, San Francisco, CA, March/April 1998
	AL	F. M. Chiussi et al, "Virtual Queueing Techniques for ABR Service: Improving ABR/VBR Interaction", <i>Proc. IEEE INFOCOM '97 - Driving The Information Revolution</i> , Vol. 1, pp 406-418, Kobe, Japan, April 1997

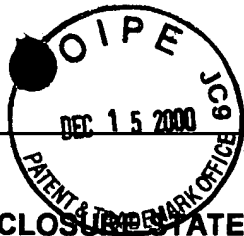
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	AM	F. Toutain, "Decoupled Generalized Processor Sharing: A Fair Queuing Principle for Adaptive Multimedia Applications", <i>Proc. IEEE INFOCOM '98 - Gateway to the 21st Century</i> , Vol. 1, pp 291-298 San Francisco, CA, March/April 1998
	AN	A. K. Parekh et al, "A Generalized Processor Sharing Approach to Flow Control in Integrated Services Networks: The Single-Node Case", <i>IEEE/ACM Transactions on Networking</i> , Vol. 1, No. 3, June 1993, pp 344-357

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